

PTO/SB/08A (08-03)

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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	10/747,737
				Filing Date	12/29/2003
				First Named Inventor	Bailey et al.
				Group Art Unit	1791
				Examiner Name	Emmanuel Luk
Sheet	1	of	4	Attorney Docket Number	PA97-39D13D21

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			

	D1	5,515,167		Ledger et al.	5/7/1996	
	D2	5,669,303		Maracas et al.	9/23/1997	
	D3	2002/0150398		Choi et al.	10/17/2002	
	D4	6,713,238		Chou et al.	3/30/2004	
	D5	5,324,012		Aoyama et al.	6/28/1994	
	D6	4,551,192		DiMilia et al.	11/5/1985	
	D7	5,534,073		Kinoshita et al.	7/9/1996	
	D8	2004/0250945		Zheng et al.	12/16/2004	
	D9	2005/0037143		Chou et al.	2/17/2005	
	D10	6,900,881		Sreenivasan et al.	5/31/2005	
	D11	6,902,853		Sreenivasan et al.	6/7/2005	
	D12	6,908,861		Sreenivasan et al.	6/21/2005	
	D13	6,916,584		Sreenivasan et al.	7/12/2005	
	D14	6,921,615		Sreenivasan et al.	7/26/2005	
	D15	6,916,585		Sreenivasan et al.	7/12/2005	
	D16	6,919,152		Sreenivasan et al.	7/19/2005	
	E1	6,753,131	B1	Rogers et al.	6/22/2004	
	E2	6,388,755	B1	Zhao	5/14/2002	

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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

	D17	APPLICATION NO. 11/047,499, naming Inventors Choi et al., entitled Method of Retaining a Substrate to a Wafer Chuck, filed 1/31/2005	
	D18	APPLICATION NO. 11/108,208, naming Inventors Choi et al., entitled Method of Separating a Mold from a Solidified Layer Disposed on a Substrate, filed 4/18/2005	
	D19	APPLICATION NO. 11/126,946, naming Inventors Choi et al., entitled Formation of Discontinuous Films During an Imprint Lithography Process, filed 5/11/2005	
	D20	APPLICATION NO. 11/127,041, naming Inventors Sreenivasan et al., entitled Step and Repeat Imprint Lithography Processes, filed 5/11/2005	
	D21	APPLICATION NO. 11/127,060, naming Inventors Sreenivasan et al., entitled Step and Repeat Imprint Lithography Systems, filed 5/11/2005	
	D22	APPLICATION NO. 11/136,885, naming Inventors Nimmakayala et al., entitled Substrate Support System, filed 5/25/2005	
	D23	APPLICATION NO. 11/136,891, naming Inventors GanapathiSubramanian et al., entitled Adaptive Shape Substrate Support System, filed 5/25/2005	
	D24	APPLICATION NO. 11/136,886, naming Inventors Nimmakayala et al., entitled Substrate Support Method, filed 5/25/2005	
	D25	APPLICATION NO. 11/136,892, naming Inventors GanapathiSubramanian et al., entitled Adaptive Shape Substrate Support Method, filed 5/25/2005	

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	E11	PRINCE ET AL., Semiconductor Memories, 2nd E., Wiley, pp. 9, 90, 91 1/1/1983	

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